Tensmitted herewith for filing is the Patent Application of:

Inversor: KUO-CHI TU, WEN-TING CHU, YI-SHING CHANG, YI-JIUN LIN

For: SPACER PROCESS TO PREVENT THE REVERSE TUNNELING IN SPLIT GATE FLASH

17497 U.S. PTO 10/786798

Enclosed are:							
x	6 sheets of drawing(s) - formal.						
x	An assignment of the invention to Taiwan Semiconductor Manufacturing Co.						
	An associate power of attorney Applicant claims small entity status						
	Request & Certification under 35 USC 122(b)(2)(b)(i)						
The filing fee has been calculated as shown below:							
(Col.		(Col. 1)	(Col. 2)	OTHER THAN A	A SMALL ENTITY		
FOR:		NO. FILED	NO. EXTRA	RATE	FEE		
BASIC	FEE	$\searrow$	$\nearrow$		\$ 770.		
TOTA	L CLAIMS	<b>27 -</b> 20=	7	x 18 =	\$ 126.		
INDEF	CLAIMS	2 -3=	0	x 86 =	\$ 0.		
				SUB TOTAL	\$ 896.		
				ASSIGNMENT	\$40.		
				TOTAL	\$ 936.		
X	Please charge my Deposit Account No. 19-0033 in the amount of \$ 936. A duplicate copy of this sheet is enclosed.						
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## **EXPRESS MAIL CERTIFICATE**

## Express Mail No. EV385357862US

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or-Attorney requests the date of deposit as the Filing Date.

of deposit Signature / Date